



**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of: )

Teruyuki MAEDA )

) Group Art Unit: 2814

Serial No.: 10/735,399 )

) Examiner: PHAM, Long

Filed: December 11, 2003 )

For: **POWER TRANSISTOR AND  
SEMICONDUCTOR INTEGRATED CIRCUIT USING THE SAME**

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**AMENDMENT**

Sir:

In response to the Office Action dated March 10 2005, please amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 5 of this paper.

Certificate of Express Mailing

I hereby certify that this correspondence is being deposited with the United States Postal Service as Express Mail, Airbill No. EV 438976785 US, with sufficient postage in an envelope addressed to: Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on

August 9, 2005  
Date of Deposit

Donna R. Davis  
Donna R. Davis